FIG. 1

LEAD-IN	D SOOD IDTION	HEE DUDDOSE	
AREA	DESCRIPTION	USE PURPOSE	
READ-ONLY ZONE (5)	PITS OR HIGH FREQUENCY WOBBLES	DISC—RELATED CONTROL DATA	
RECORDABLE ZONE (10)	DMA (10a)	DEFECT MANAGEMENT	
	CONTROL DATA ZONE (10b)	DATA INFORMATION (CONTROL DATA)	
	OPC TEST ZONE (10c)	OPC TESTING (OPC DATA)	
	DRIVE & DISC ZONE (10d)	INFORMATION ON DRIVE AND INFORMATION ON THE STATE OF DISC	
	BUFFER ZONE (10e)	BUFFER FOR USER DATA AREA	

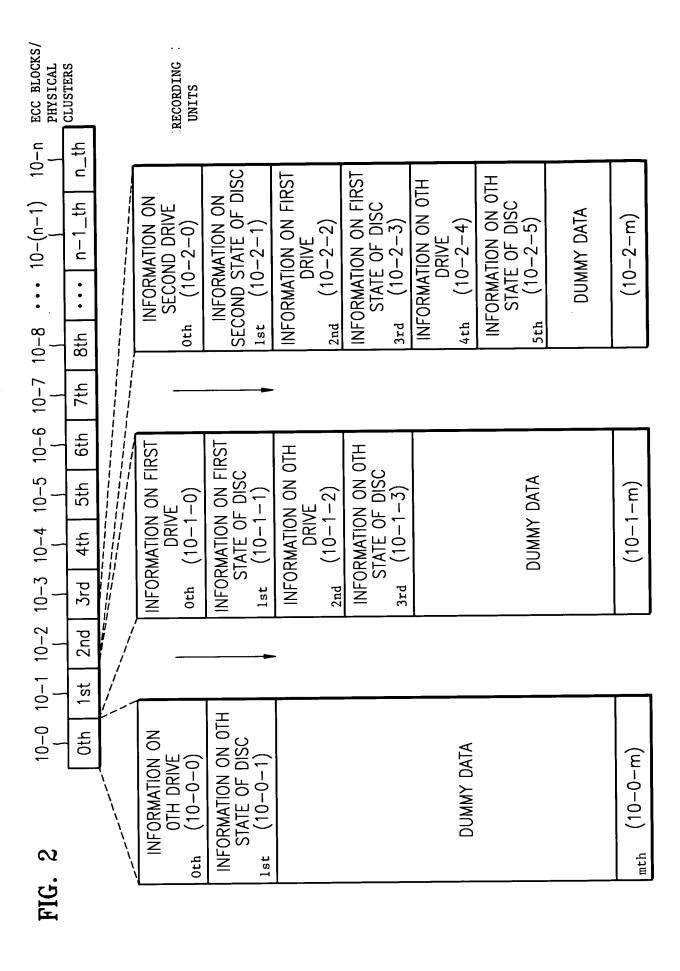
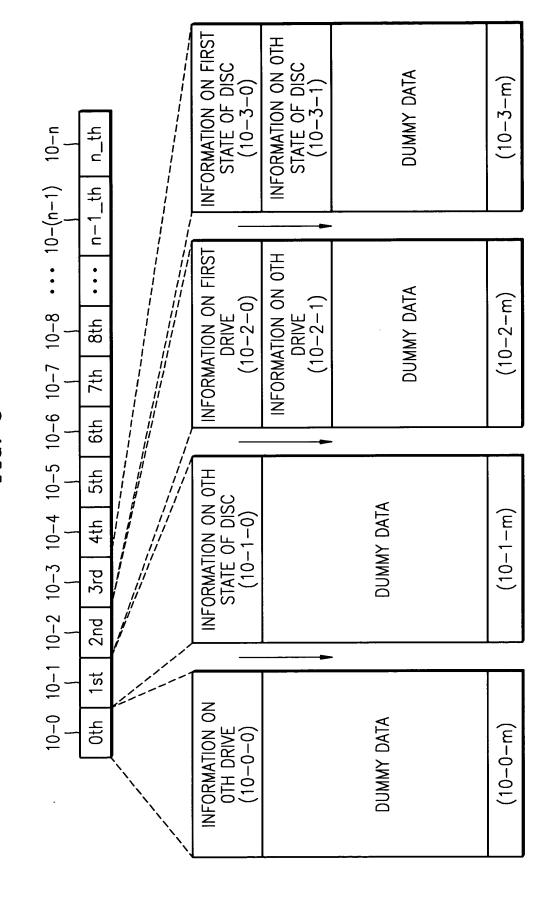


FIG. 3



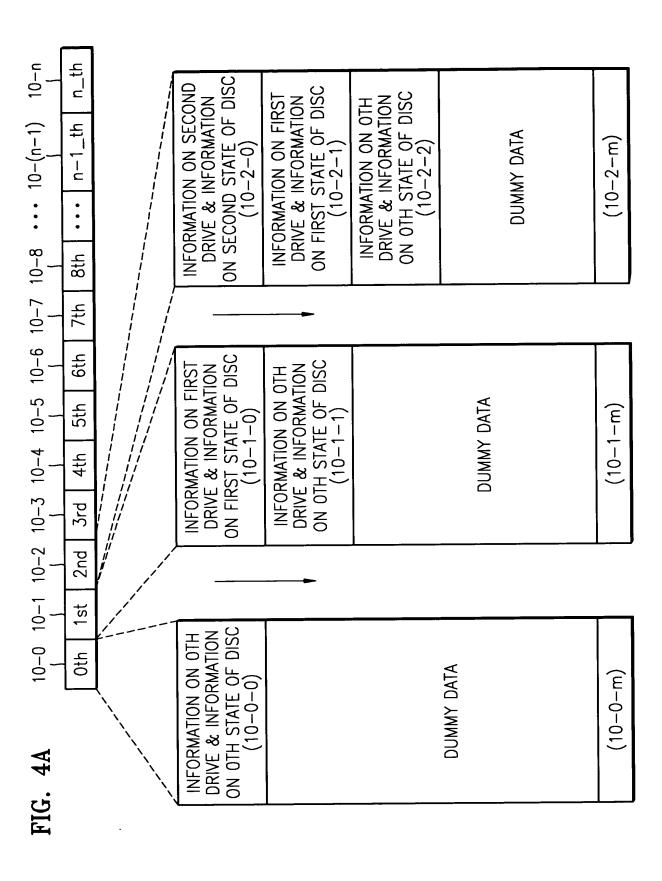


FIG. 4B

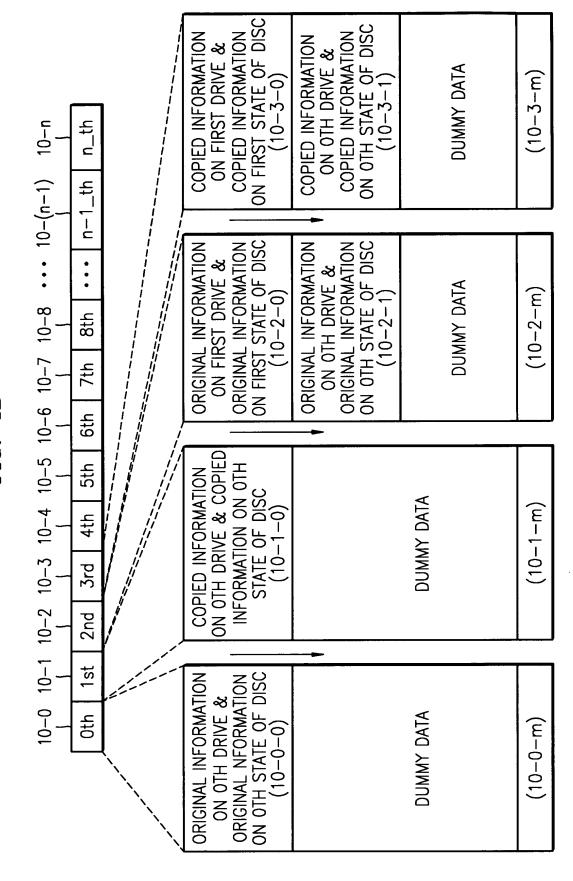


FIG. 5

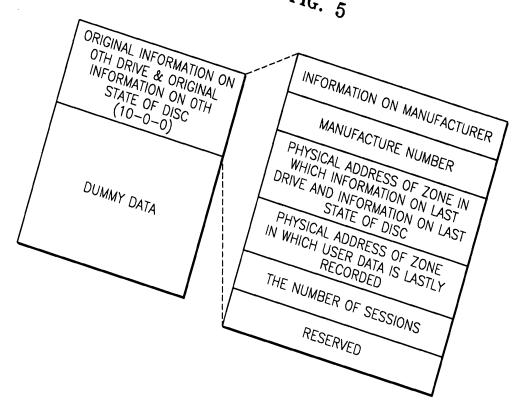
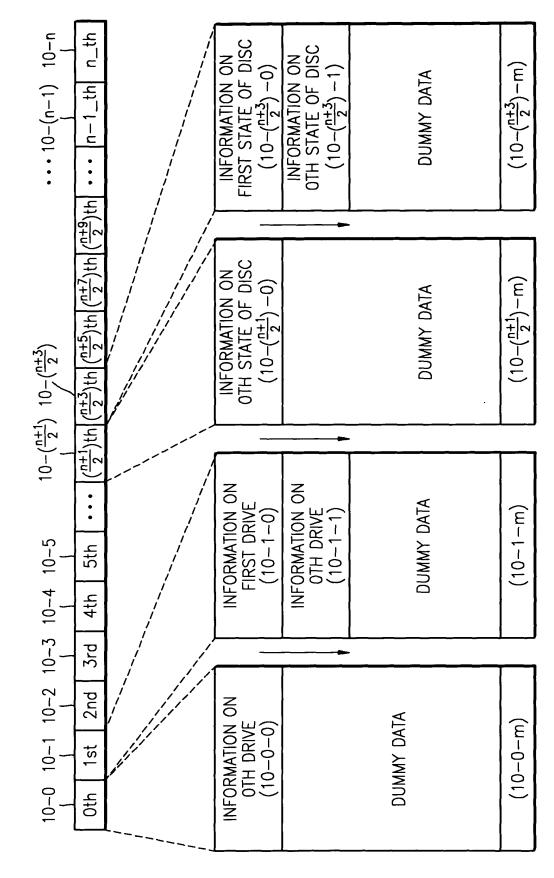


FIG. 6A

LEAD-IN AREA	DESCRIPTION	USE PURPOSE	
READ-ONLY ZONE (5)	PITS OR HIGH FREQUENCY WOBBLES	DISC-RELATED CONTROL DATA	
RECORDABLE ZONE (10)	DMA (10a)	DEFECT MANAGEMENT	
	CONTROL DATA ZONE (10b)	DATA INFORMATION (CONTROL DATA)	
	OPC TEST ZONE (10c)	OPC TESTING (OPC DATA)	
	DRIVE & DISC ZONE	DRIVE-RELATED INFORMATION	
	(10d)	DISC STATE—RELATED INFORMATION	
	BUFFER ZONE (10e)	BUFFER FOR USER DATA ZONE	

FIG. 6B



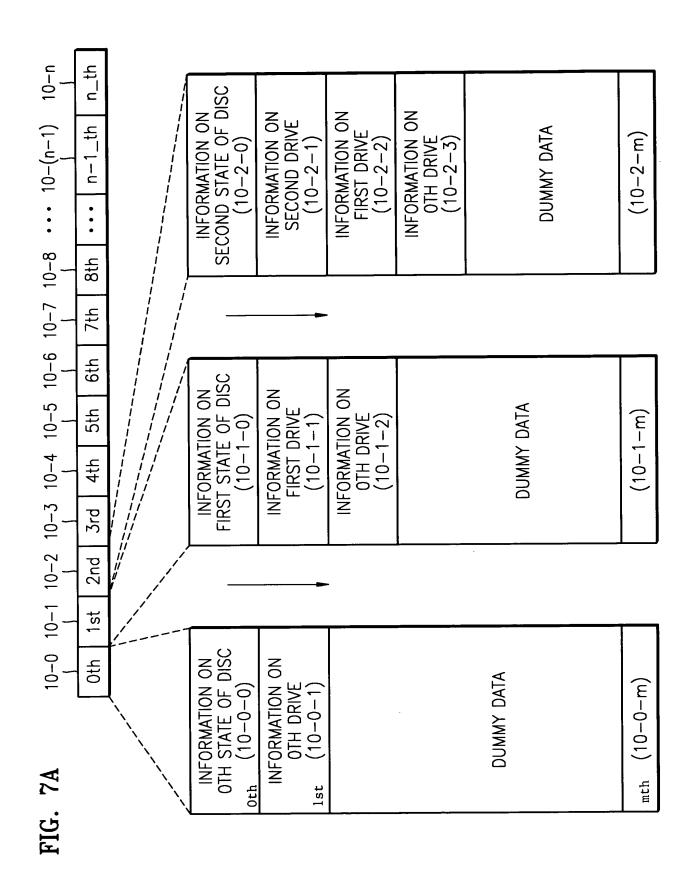


FIG. 7B

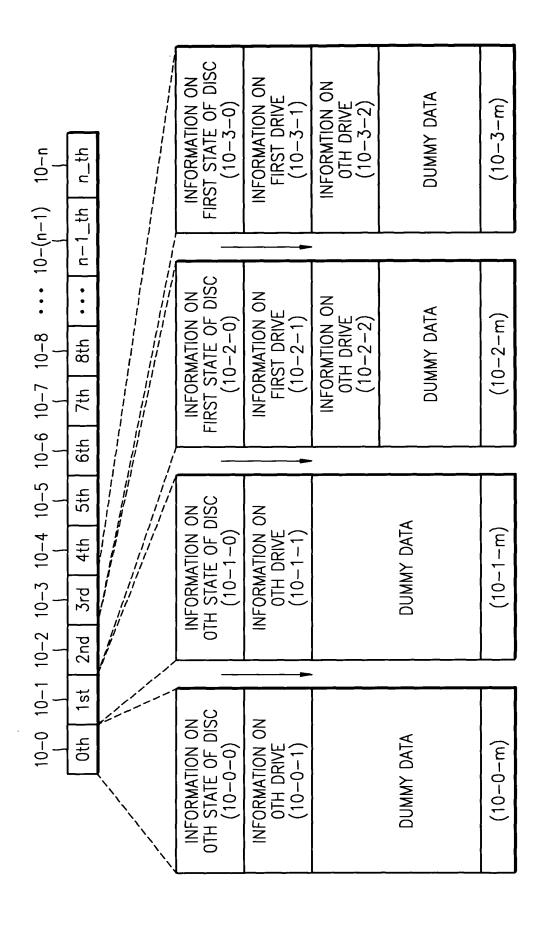


FIG. 8A

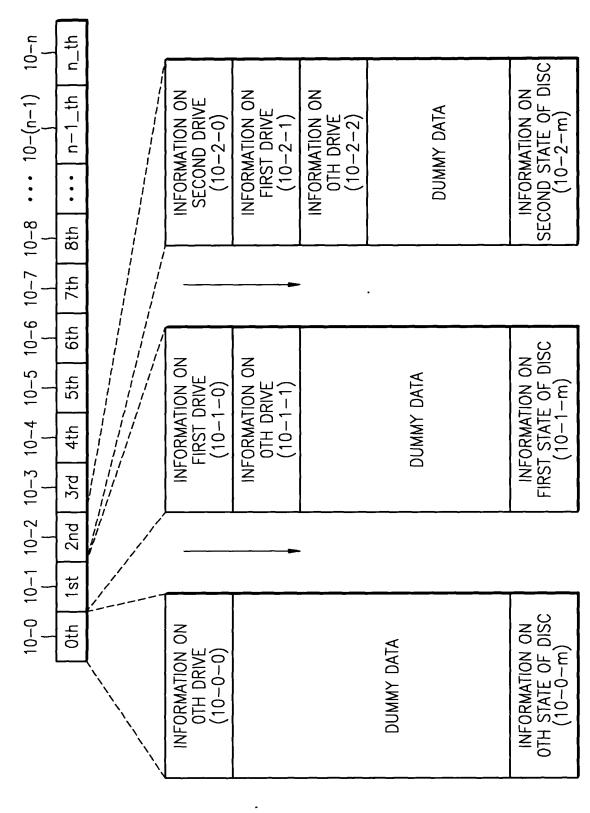


FIG. 8B

10-(n-1) 10-n	INFORMATION ON FIRST DRIVE (10-3-0)	INFORMATION ON OTH DRIVE (10-3-1)	DUMMY DATA	INFORMATION ON FIRST STATE OF DISC (10-3-m)
i //	INFORMATION ON FIRST DRIVE (10-2-0)	INFORMATION ON OTH DRIVE (10-2-1)	DUMMY DATA	INFORMATION ON FIRST STATE OF DISC (10-2-m)
10-0 10-1 10-2 10-3 10-4 10-5 10-6 10-7 10-8 Oth 1st 2nd 3rd 4th 5th 6th 7th 8th	INFORMATION ON OTH DRIVE (10-1-0)		DUMMY DATA	INFORMATION ON OTH STATE OF DISC (10-1-m)
10-0 10-1 Oth 1st	INFORMATION ON OTH DRIVE (10-0-0)		DUMMY DATA	INFORMATION ON OTH STATE OF DISC (10-0-m)

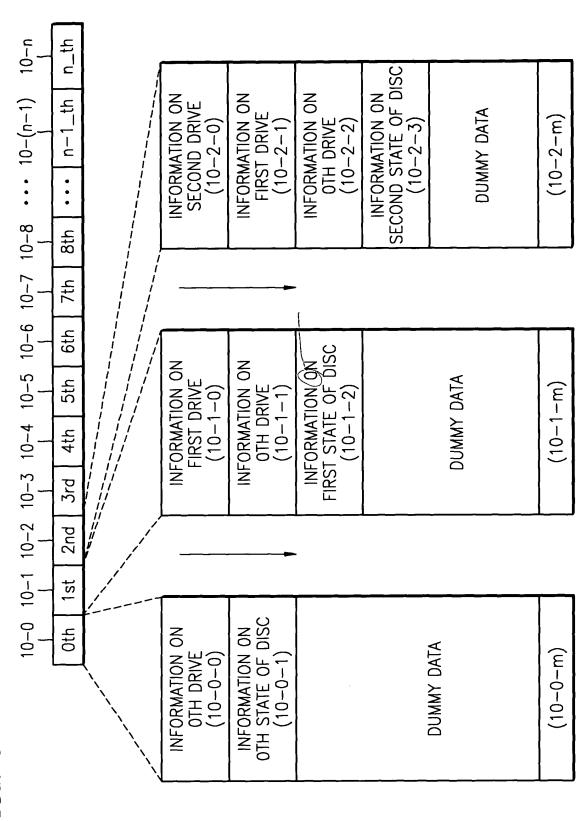


FIG. 10A

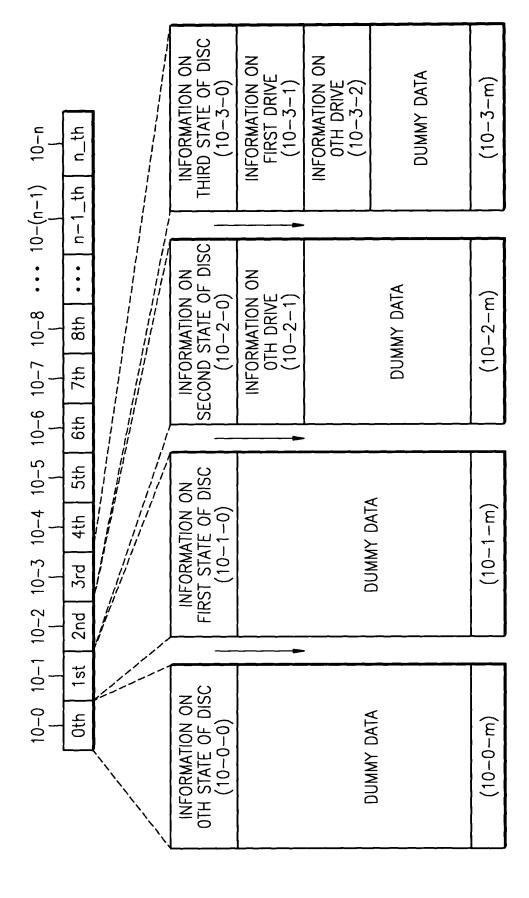


FIG. 10B

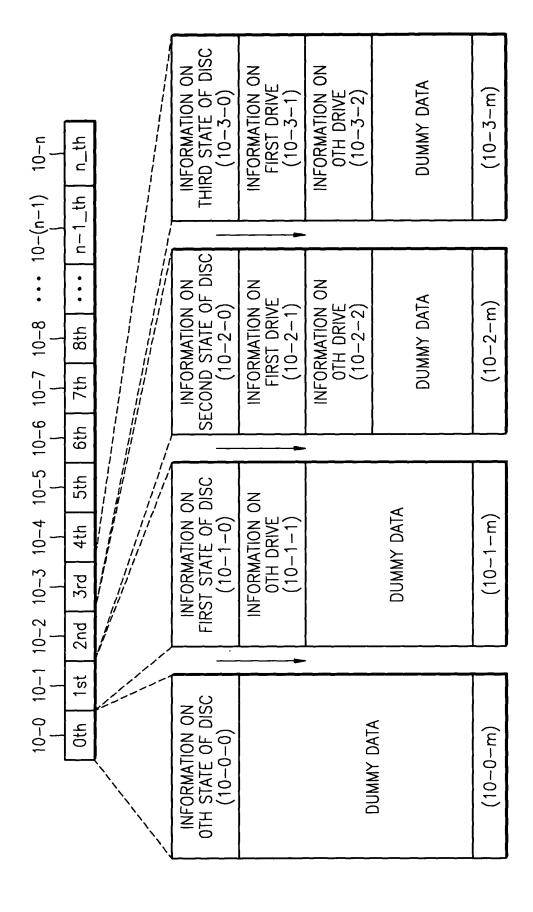


FIG. 10C

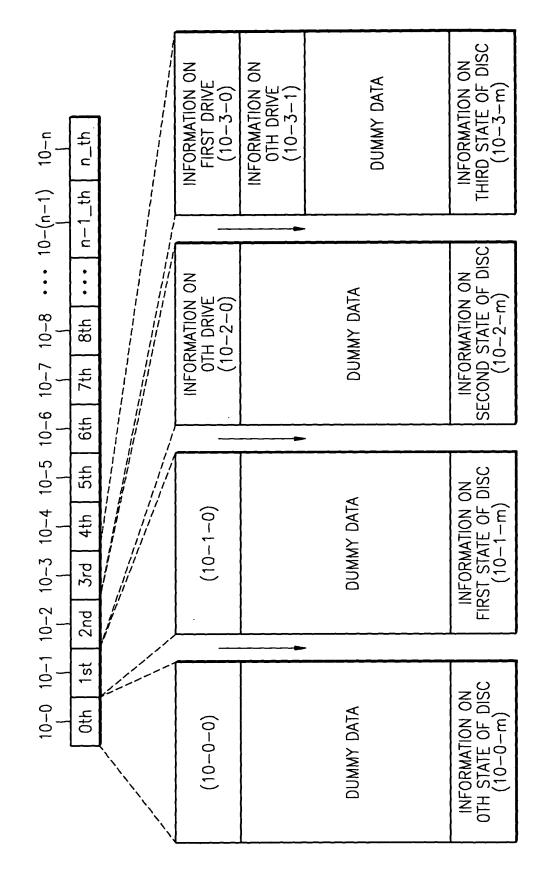


FIG. 11A

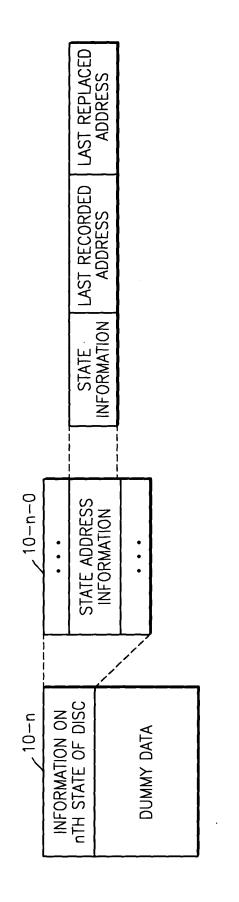


FIG. 11B

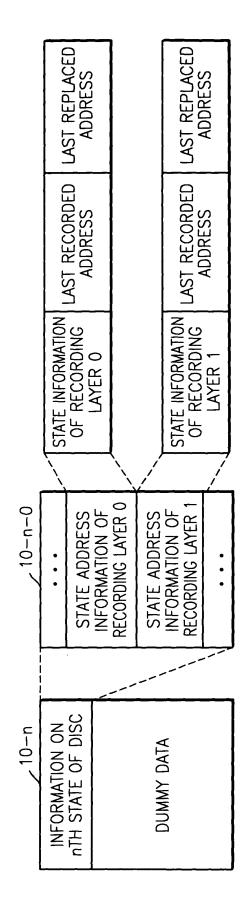


FIG. 11C

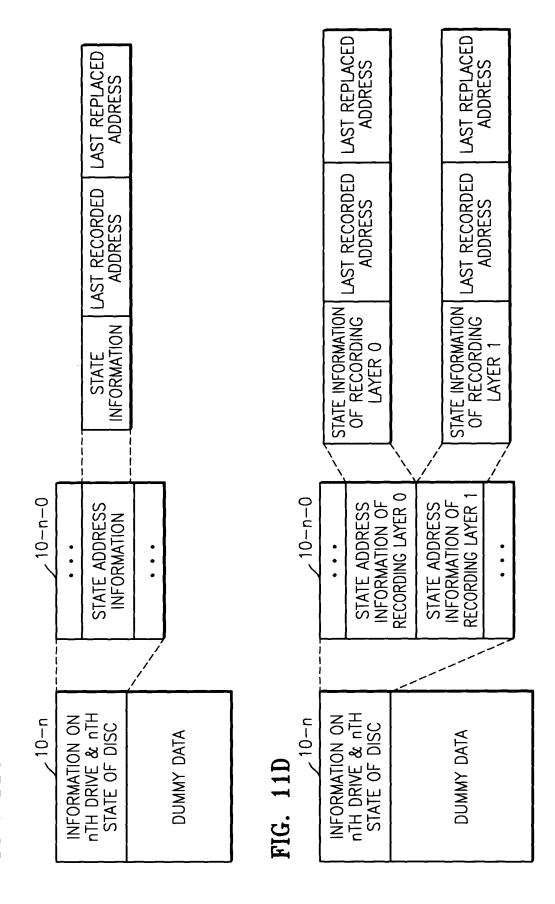


FIG. 12A

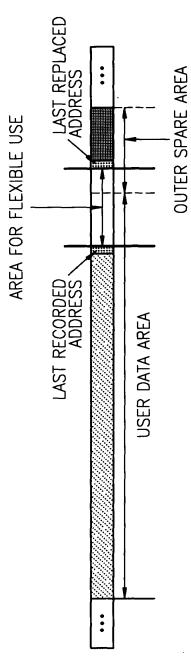


FIG. 12B

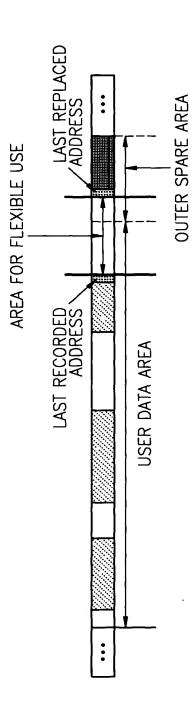


FIG. 13A

LEAD-IN AREA

INNER SPARE AREA 0

USER DATA AREA

OUTER SPARE AREA O

LEAD-OUT AREA

FIG. 13B

LEAD-IN AREA

INNER SPARE AREA 0

USER DATA AREA

OUTER SPARE AREA 0

OUTER AREA 0

RECORDING LAYER 0

LEAD-OUT AREA

INNER SPARE AREA 1

USER DATA AREA

OUTER SPARE AREA 1

OUTER AREA 1

RECORDING LAYER 1

FIG. 14A

8BITS	28BITS	28BITS
STATE INFORMATION	LAST RECORDED ADDRESS	LAST REPLACED ADDRESS

FIG. 14B

4BITS	28BITS	4BITS	28BITS
STATE INFORMATION 1	LAST RECORDED ADDRESS	STATE INFORMATION 2	LAST REPLACED ADDRESS

FIG. 14C

8BITS	28BITS	28BITS
STATE INFORMATION OF RECORDING LAYER 0	LAST RECORDED ADDRESS	LAST REPLACED ADDRESS
STATE INFORMATION OF RECORDING LAYER 1	LAST RECORDED ADDRESS	LAST REPLACED ADDRESS

FIG. 14D

4BITS	28BITS	4BITS	28BITS
STATE INFORMATION OF RECORDING LAYER 0	LAST RECORDED ADDRESS	STATE INFORMATION 2 OF RECORDING LAYER 0	LAST REPLACED ADDRESS
STATE INFORMATION OF RECORDING LAYER 1	LAST RECORDED ADDRESS	STATE INFORMATION 2 OF RECORDING LAYER 1	LAST REPLACED ADDRESS